

- 22 -

ABSTRACT

A method and system for synchronizing a receiver's clock to the clock of a transmitter is disclosed herein. The disclosed system employs a digital clock synthesizer that uses
5 patterns superimposed upon a receiver oscillator to synthesize a clock rate that approximates the clock rate of the transmitter. The pattern superimposed upon the receiver oscillator can be varied to allow for tracking of the variation in the transmitter clock.

4-13-01 03:10 FAX 613 787 3558